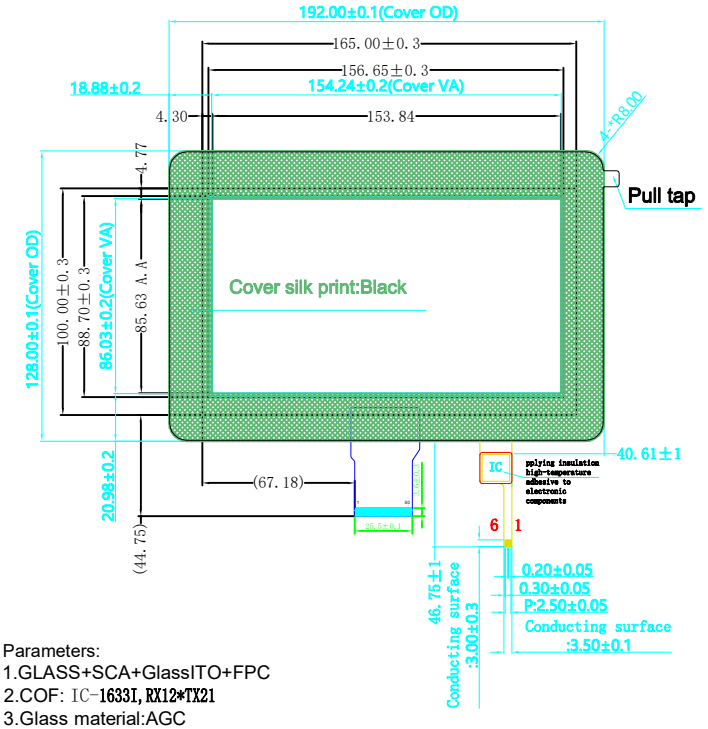
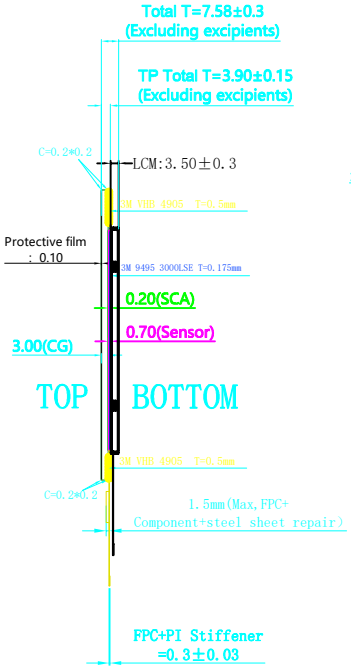
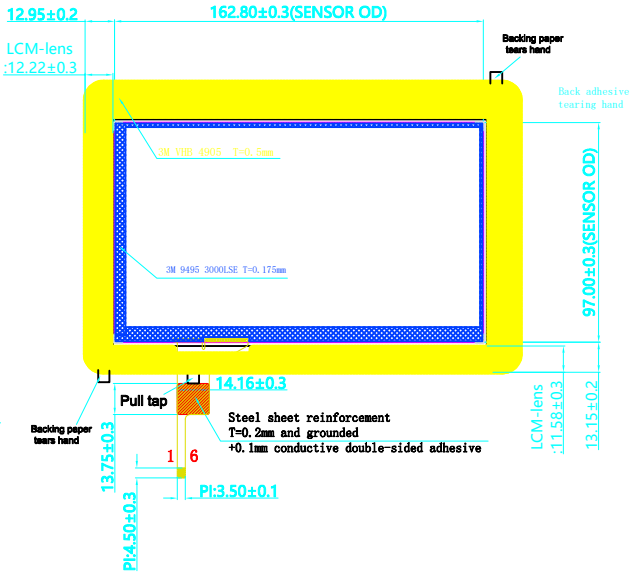
Front View



Side View



Back View



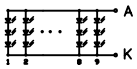
PIN	SYMBOL	PIN	SYMBOL
31	R4	1	VLED+
32	R3	2	VLED+
33	R2	3	VLED+
34	R1	4	VLED+
35	R0	5	GND
36	GND	6	NC
37	DCLK	7	DVDD
38	GND	8	NC
39	SHLR	9	DE
40	UPDN	10	VSYNC
41	NC	11	HSYNC
42	NC	12	B7
43	NC	13	B6
44	RESET	14	B5
45	DISP	15	B4
46	NC	16	B3
47	NC	17	B2
48	GND	18	B1
49	NC	19	B0
50	NC	20	G7
		21	G6
		22	G5
		23	G4
		24	G3
		25	G2
		26	G1
		27	G0
		28	R7
		29	R6
		30	R5

IIC-TP logic definition:

PIN	Connection
1	RST
2	SDA
3	SCL
4	INT
5	VCC
6	GND

Parameters:
1.GLASS+SCA+GlassITO+FPC
2.COF: IC-1633I,RX12*TX21
3.Glass material:AGC
4.Light transmittance rate ≥85%
5.Cover hardness≥6H
6.TP operation Temp.:-20℃~+70℃，≤90%RH
7.TP storage temp.:-30℃~+80℃，≤90%RH
8.ROHS compliance
9.Unmarked tolerance±0.2mm

Specification:
1). Display mode: 16.7M, TFT/Transmissive
2). Operating temp.: -20℃~+70℃
Storage temp.: -30℃~+80℃
3). Backlight: 27 hip White LED
4). Dimensions with mark "*" are important, with mark "()" are referenced



LED Circuit diagram
Vf 9.0/TYP IF=20*9=180mA(Constant current test)

Engineer Drawing			MAXEN ELECTRONICS LIMITED			
Direction			Model No.	MX-070WR50KFM-N74-C2572	Tolerance	±0.20
Prepared By	Auditor	Date	Client No.		Unit	mm
			Date		Ratio	1 : 1