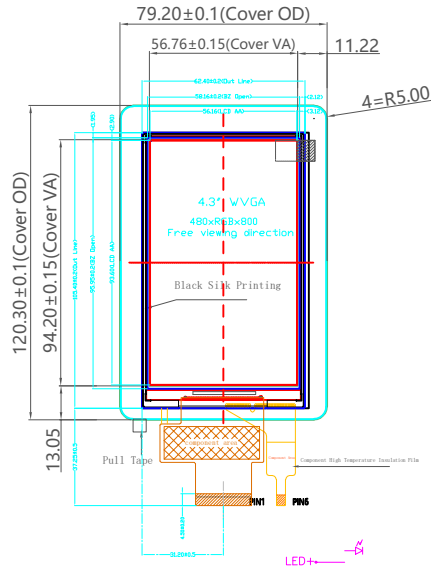


# Front View

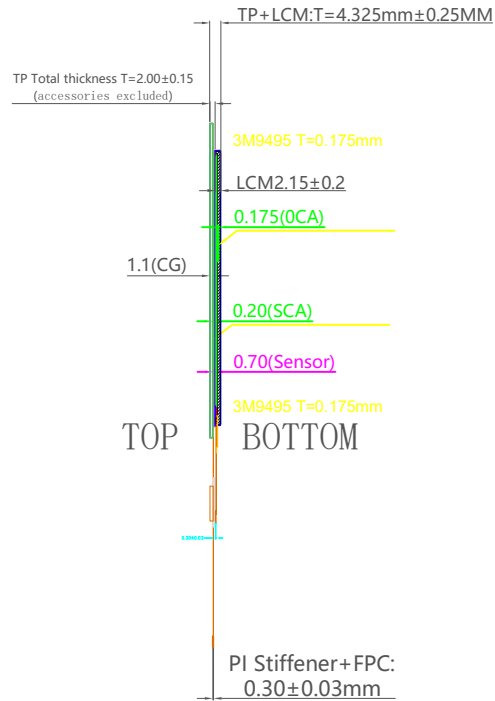


## Technical Parameters:

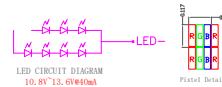
- GLASS+SCA+GlassITO+FPC
- GG/COF Structure: GT911
- Glass Material: Asahi Glass
- Glass Transparency:  $\geq 85\%$
- Glass Surface Hardness:  $\geq 6H$
- TP Working Environment:  $-10^{\circ}\text{C} \sim +60^{\circ}\text{C}$ ,  $\leq 90\%RH$
- TP Storage Environment:  $-20^{\circ}\text{C} \sim +70^{\circ}\text{C}$ ,  $\leq 90\%RH$
- ROHS compliant
- Unmarked Tolerance  $\pm 0.2\text{mm}$

NOTES:  
 1. Overall tolerance:  $\pm 0.2$   
 2.  $\phi$  reference dimension.  
 3. Recommended Case Open Area Should be Less Than Module VA  
 4. RHD:  $\mu\text{m} \pm 10\%$  (max)

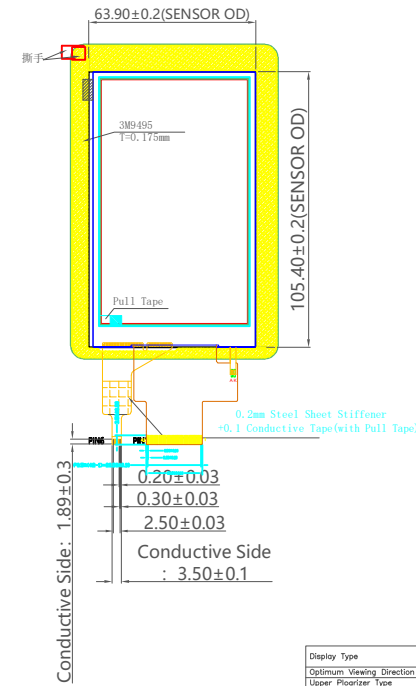
# Side View



# (G+G Structure)



# Back View



TP Pin Assignment		No.	Symbol
Display Type	JPS TFT	1	INT
Optimum Viewing Direction	NORMALLY BLACK	2	SDA
Upper Polarizer Type	Clear	3	SCK
LED Driver IC	LUMINOSE	4	RST
Operating Voltage	VCC=3.3V	5	VCC
Operation Temperature	-20°C TO 70°C	6	GND
Storage Temperature	-30°C TO 80°C		
Interface	MPI/RS485		
Backlight	8-CHP WHITE LED		
Surface luminance	350 cd/m <sup>2</sup> (Typ.)		
White X/Y			

客户签名:

日期:

PIN# ASSIGNMENT

1	LED_A
2	LED_K
3	GND
4	VDDQ=3.3V
5	VDDIO
6	GNP
7	HSSI_DP_N
8	HSSI_DP_P
9	GND
10	HSSI_CLK_N
11	HSSI_CLK_P
12	GNP
13	HSSI_BI_N
14	HSSI_BI_P
15	GND
16	BO
17	BI
18	BE
19	BA
20	BA
21	BS
22	GS
23	GI
24	GS
25	GS
26	GS
27	GS
28	BO
29	BI
30	BE
31	K3
32	K4
33	RS
34	DOTCLK
35	HSYNC
36	VSYNC
37	BE
38	TE
39	SPICSS
40	SPIFCS
41	SPIERAT
42	RESET

NO.	更改内容	日期
1		
2		
3		
4		
5		
6		
7		

工程确认图

MAXEN ELECTRONICS LIMITED

公司编号	MX-N43HR42-IT30-F011	未注公差	±0.20
客户编号		单位	mm
版本号		比例	1 : 1

方向

绘图	审核	日期
		20220729