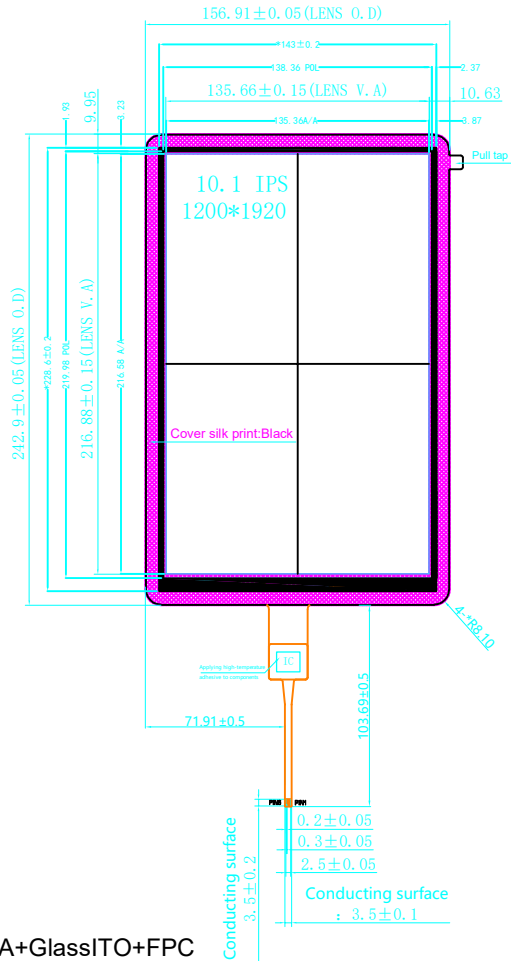
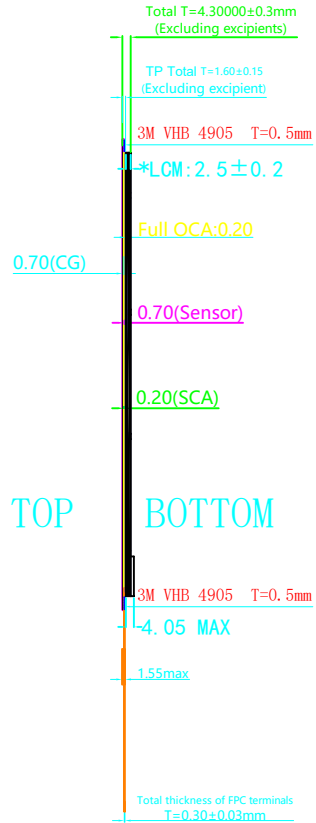


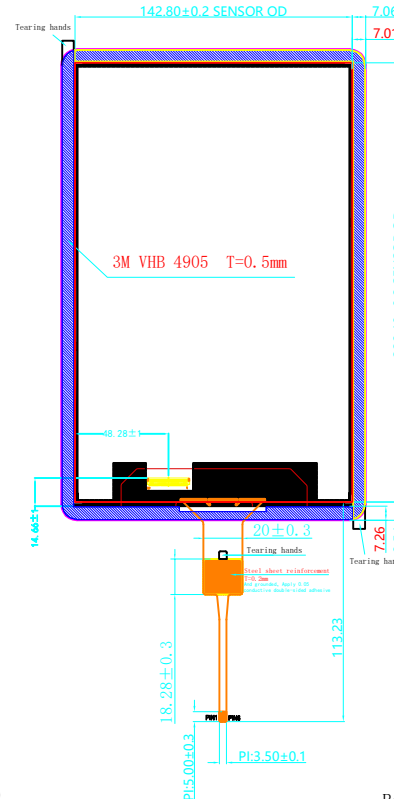
Front View



Side View



Back View

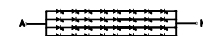


	LCD (JA9)
1	NC
2	VDD (3.3V)
3	VDD (3.3V)
4	NC
5	RST 3.3V
6	NC
7	GND
8	MIP1 D0-
9	MIP1 D0+
10	GND
11	MIP1 D1-
12	MIP1 D1+
13	GND
14	MIP1 CLK-
15	MIP1 CLK+
16	GND
17	MIP1 D2-
18	MIP1 D2+
19	GND
20	MIP1 D3-
21	MIP1 D3+
22	GND
23	NC
24	NC
25	NC
26	NC
27	NC
28	NC
29	NC
30	NC
31	VLED-
32	VLED-
33	NC
34	NC
35	NC
36	NC
37	NC
38	NC
39	VLED+
40	VLED+

IIC Definition

PIN	I2C定义
1	VDD
2	REST
3	SCL
4	SDA
5	INT
6	GND

Back light circuit diagram:



Parameters:

- GLASS+SCA+GlassITO+FPC
- COF: GT9271
- Glass material:AGC
- Light transmittance rate ≥86%
- Cover hardness≥6H
- TP operation Temp.: -20°C~+70°C, ≤90%RH
- TP storage temp.: -30°C~+80°C, ≤90%RH
- ROHS compliance
- Unmarked tolerance±0.2mm

Notes:

- Unit:mm
- Do not scale drawing.
- Modification rev.number.
- General Tolerance:±0.2
- *** For important dimension;() for reference dimation
- RoHs must be complied.(Use Lead-free process)

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Luminous (Without PET)	IV	350	400		cd/m ²
Chromaticity Coordinates Without PET	X	(0.280)	(0.340)		
Uniformity	Y	70	75		%
Backlight voltage	V _F	22.4	24	25.0	V
Backlight current	I _F		92		mA

Engineer Drawing

Direction			MAXEN ELECTRONICS LIMITED			
Prepared By	Auditor	Date	Model No.	MX-A101FM40-IL34-F1928	Tolerance	±0.20
			Client No.		Unit	mm
			Date	2023-11-3	Ratio	1 : 1

A

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