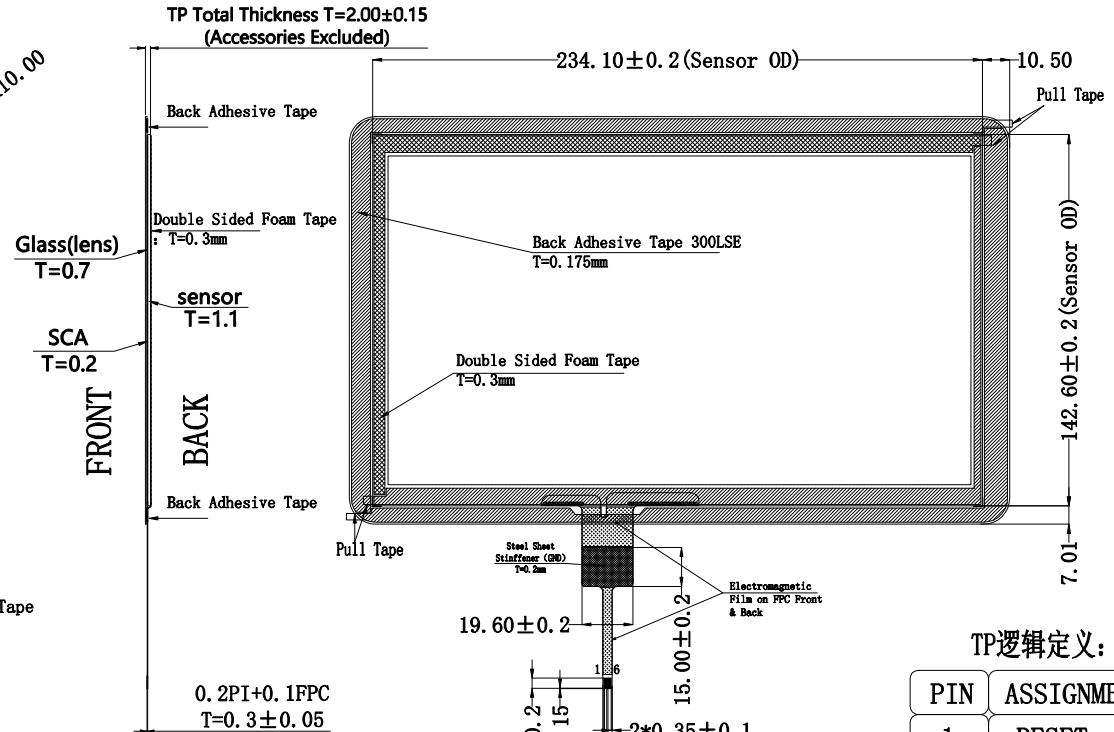
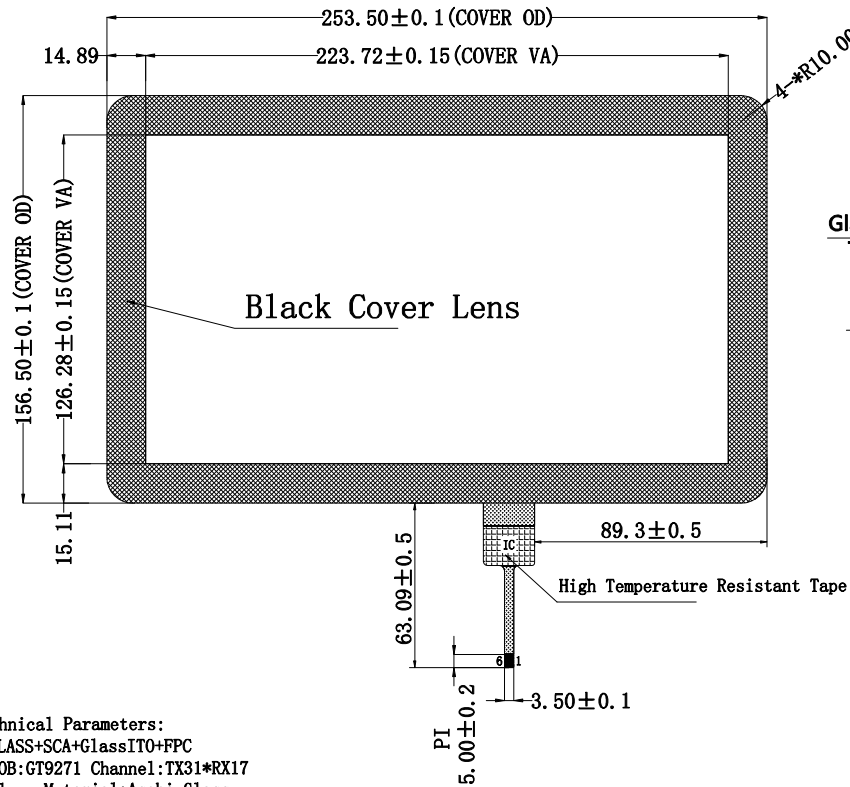


Front View

Side View Back View

客户签名:

日期:



- Technical Parameters:
1. GLASS+SCA+GlassITO+FPC
 2. COB:GT9271 Channel:TX31*RX17
 3. Glass Material:Asahi Glass
 4. Glass Transparency \geq 85%
 5. Glass Surface Hardness: \geq 6H
 6. TP Operating Environment: $-10^{\circ}\text{C}\sim+50^{\circ}\text{C}$, \leq 90%RH
 7. TP Storage Environment: $-20^{\circ}\text{C}\sim+70^{\circ}\text{C}$, \leq 90%RH
 8. ROHS Compliant
 9. Unmarked Tolerance \pm 0.2mm

(G+G Structure)

TP逻辑定义:

PIN	ASSIGNMENT
1	RESET
2	SDA
3	SCL
4	INT
5	VDD (3.3V)
6	GND

TX31*RX17

物料	NO.	更改内容	日期	工程确认图			MAXEN ELECTRONICS LIMITED				
	①			方向		公司编号	HCT651GG101C1J2-V0	未注公差	\pm 0.20		
	②										
	③			绘图	审核	日期	客户编号	单位	mm		
	④										
	⑤					20211222	版本号	比例	1 : 1		
	⑥										
	⑦										

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