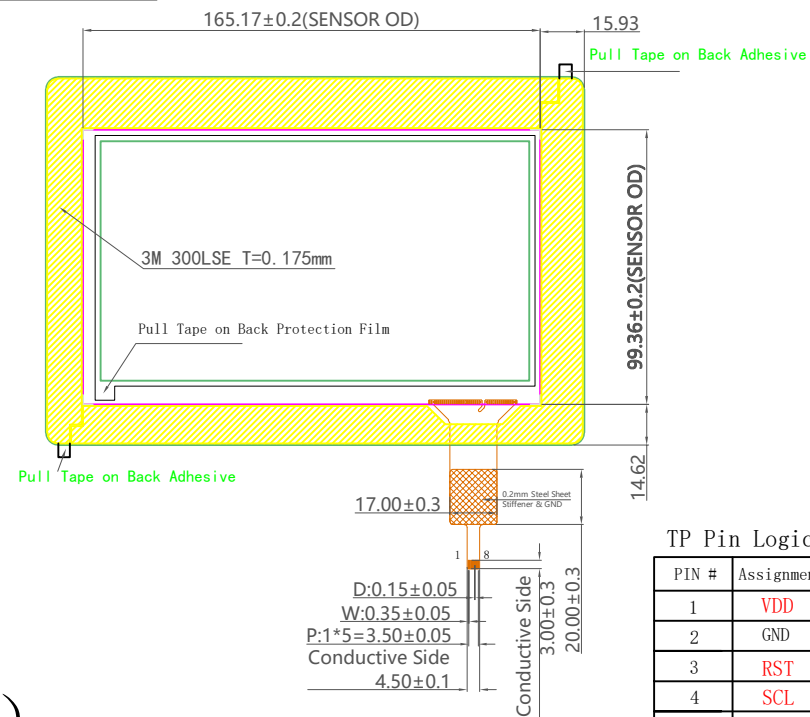
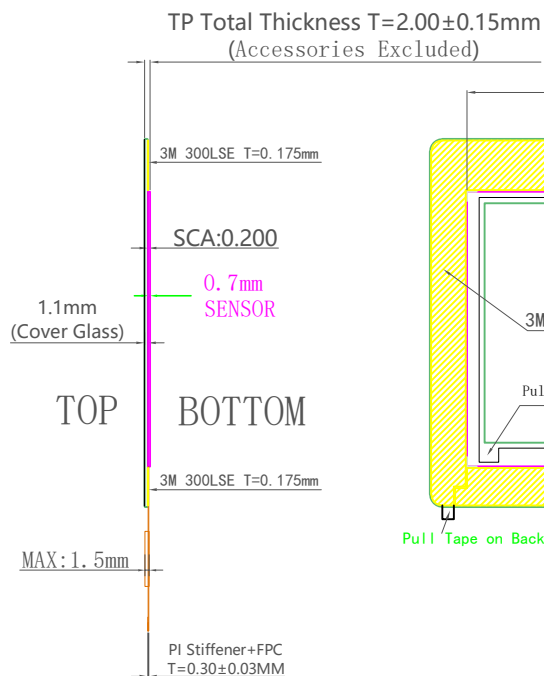
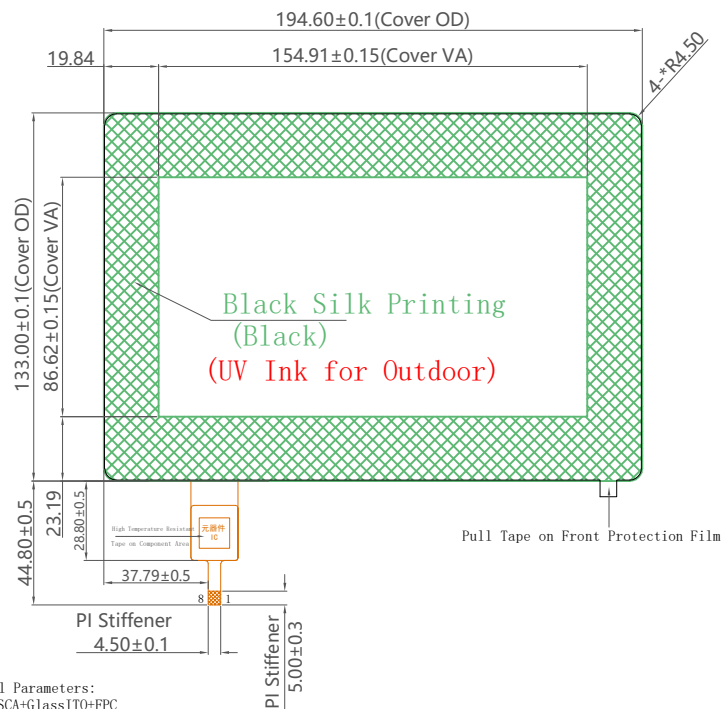


Front View Side View Back View

客户签名:

日期:



TP Pin Logic

PIN #	Assignment
1	VDD
2	GND
3	RST
4	SCL
5	SDA
6	INT
7	NC
8	NC

(G+G Structure)

- Technical Parameters:
- GLASS+SCA+GlassITO+FPC
 - GG/COF Structure:HY4633, RX12*TX22;
 - Cover Lens Material:Asahi Glass
Surface Hardness $\geq 6H$
 - Cover Lens Transparency: $\geq 85\%$
 - Cover Lens Hardness: $\geq 6H$
 - TP Operating Environment: $-20^{\circ}C \sim +70^{\circ}C$, $\leq 90\%RH$
 - TP Storage Environment: $-30^{\circ}C \sim +80^{\circ}C$, $\leq 90\%RH$
 - ROHS Compliant
 - Unmarked Tolerance: $\pm 0.2mm$

物料	NO.	更改内容	日期	工程确认图		MAXEN ELECTRONICS LIMITED				
	△			方向		公司编号	HCT1697GG070A1-V0	未注公差	± 0.20	
	△					绘图	审核	日期	客户编号	单位
	△					20220526	版本号	比例	1 : 1	

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